L Number	Hits	Search Text	DB	Time stamp
2	1	6185428.pn. and processor	USPAT; EPO; JPO	2003/09/29 12:40
3	9	(insert$3 or add$3) same delay$3 and (memory or buffer$3) same (shift near4 register) same cyclical$3 with address$3	USPAT; EPO; JPO	2003/09/29 12:43
5	56	(insert$3 or add$3) same delay$3 and (memory or buffer$3) same (shift near4 register) and cyclical$3 with address$3	USPAT; EPO; JPO	2003/09/29 12:45
7	40	(insert$3 or add$3) same delay$3 and (memory or buffer$3) and (shift near4 register) and cyclical$3 with address$3 with memory	USPAT; EPO; JPO	2003/09/29 12:44
9	40	((insert$3 or add$3) same delay$3 and (memory or buffer$3) and (shift near4 register) and cyclical$3 with address$3 with memory) and @ad<20000920	USPAT; EPO; JPO	2003/09/29 12:45
10	56	((insert$3 or add$3) same delay$3 and (memory or buffer$3) same (shift near4 register) and cyclical$3 with address$3) and @ad<20000920	USPAT; EPO; JPO	2003/09/29 12:45
11	5	(insert$3 or add$3) same delay$3 and (memory or buffer$3) same (shift near4 register) same cyclical$3 with address$3 with memory	USPAT; EPO; JPO	2003/09/29 12:46
12	5	((insert$3 or add$3) same delay$3 and (memory or buffer$3) same (shift near4 register) same cyclical$3 with address$3 with memory) and @ad<20000920	USPAT; EPO; JPO	2003/09/29 12:47
16	40	(insert$3 or add$3) same delay$3 and (memory or buffer$3) and (shift near4 register) and cyclical$3 with address$3 with memory	USPAT; EPO; JPO	2003/09/29 12:49
17	40	((insert$3 or add$3) same delay$3 and (memory or buffer$3) and (shift near4 register) and cyclical$3 with address$3 with memory) and @ad<20000920	USPAT; EPO; JPO	2003/09/29 12:52
18	32	(((insert$3 or add$3) same delay$3 and (memory or buffer$3) and (shift near4 register) and cyclical$3 with address$3 with memory) and @ad<20000920) and ((base near4 station) or MS)	USPAT; EPO; JPO	2003/09/29 12:49
20	1	(insert$3 or add$3) same delay$3 and (base with station) and (shift near4 register) and cyclical$3 with address$3 with memory	USPAT; EPO; JPO	2003/09/29 12:50
22	56	(base near4 station) same (memory or buffer$3) same shift with register	USPAT; EPO; JPO	2003/09/29 12:52
23	1	((base near4 station) same (memory or buffer$3) same shift with register) and cyclical$3 with address$3 with memory	USPAT; EPO; JPO	2003/09/29 12:52
24	613	(base near4 station) and (memory or buffer$3) same shift with register	USPAT; EPO; JPO	2003/09/29 13:00
25	7	((base near4 station) and (memory or buffer$3) same shift with register) and cyclical$3 with address$3 with memory	USPAT; EPO; JPO	2003/09/29 12:57
26	6	(((base near4 station) and (memory or buffer$3) same shift with register) and cyclical$3 with address$3 with memory) and @ad<20000920	USPAT; EPO; JPO	2003/09/29 12:59
27	6	((((base near4 station) and (memory or buffer$3) same shift with register) and cyclical$3 with address$3 with memory) and @ad<20000920) and delay$3	USPAT; EPO; JPO	2003/09/29 12:54
28	10	((base near4 station) and (memory or buffer$3) same shift with register) and cyclical$3 with address$3	USPAT; EPO; JPO	2003/09/29 13:01
29	9	(((base near4 station) and (memory or buffer$3) same shift with register) and cyclical$3 with address$3) and @ad<20000920	USPAT; EPO; JPO	2003/09/29 13:00
31	56	(base near4 station) same (memory or buffer$3) same shift with register	USPAT; EPO; JPO	2003/09/29 13:00

C:\APPS\EAST\Workspaces\Default EAST Workspace (Flat Panel LANDSCAPE).wsp

32	54	((base near4 station) same (memory or buffer$3) same shift with register) and @ad<20000920	USPAT; EPO; JPO	2003/09/29 13:00
34	3	(((base near4 station) same (memory or buffer$3) same shift with register) and @ad<20000920) and cyclical$3	USPAT; EPO; JPO	2003/09/29 13:03
35	2	((((base near4 station) same (memory or buffer$3) same shift with register) and @ad<20000920) and cyclical$3) and delay$3	USPAT; EPO; JPO	2003/09/29 13:03